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Filing Date	09/24/2003
First Named Inventor	Takashi SHIMIZU
Group Art Unit	Unassigned
Examiner Name	Unassigned
Attorney Docket Number	065686-0158

[illegible][illegible]

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MSJ	A3	G. SCOTT et al., "NMOS Drive Current Reduction Caused by Transistor Layout and Trench Isolation Induced Stress," IEDM Technical Digest, U.S.A., IEEE (1999) pp. 827-830.	

7/3/06

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				Application Number	10/668,974
				Filing Date	9/24/2003
				First Named Inventor	Takashi SHIMIZU
				Group Art Unit	2811
				Examiner Name	Unassigned
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Examiner Signature	Mary C. H. Col-	Date Considered	7/3/06
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¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document.

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